

**Product Summary** 

IPP (max)

1.5A

This new generation TVS is designed to protect sensitive electronics

from the damage due to ESD. The combination of small size and high

ESD surge capability makes it ideal for use in portable applications

such as cellular phones, digital cameras, and MP3 players.

VBR (min)

6V

Description

Applications

Cellular Handsets

Portable Electronics

Computers and Peripheral



# 2 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

C<sub>T (typ)</sub>

0.5pF

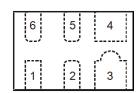
#### Features

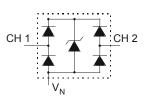
- Low Profile Package (0.61mm max) and Ultra-small PCB Footprint Area (1.68 \* 1.08mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±15kV, Contact ±15kV
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Mechanical Data**

- Case: U-DFN1610-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.003 grams (approximate)

Pin #	Description
1, 2	Input
5, 6	No Connection
3, 4	Ground





**Device Schematic** 

# Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D5V0F2U6LP-7	Standard	TG6	7	8	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

Pin Description (Top View)

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**

	TG6	
•	YM	

TG6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: B = 2014) M = Month (ex: 9 = September)

Date Code Key

Date Code Key												
Year	2014	4	2013		2014	20	15	2016		2017	2	2018
Code	В		С		D	E		F		G		Н
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	IPP	1.5	А	8/20µs (Note 7)
ESD Protection – Contact Discharge	V <sub>ESD</sub> Contact	±15	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V <sub>ESD Air</sub>	±15	kV	Standard IEC 61000-4-2

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient $T_A = +25^{\circ}C$	R <sub>0JA</sub>	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	V <sub>RWM</sub>	_	—	5.5	V	—
Channel Leakage Current (Note 6)	IR	_	_	100	nA	$V_R$ = 5V, Any I/O to GND
Reverse breakdown voltage	V <sub>BR</sub>	6.0	—	—	V	I <sub>R</sub> = 1mA
Clamping Voltage, Positive Transients (Note 7)	Vc	_	10	12	V	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20µs
Channel Input Canacitanae (Nate 9)	Ст	_	0.5	_	pF	V <sub>R</sub> = 0V, f = 1MHz, Any I/O to GND
Channel Input Capacitance (Note 8)		CT	_	0.4	0.65	рг
Dynamic Resistance	R <sub>DYN</sub>	_	0.9	_	Ω	I <sub>PP</sub> = 1A, t <sub>p</sub> = 8/20µs

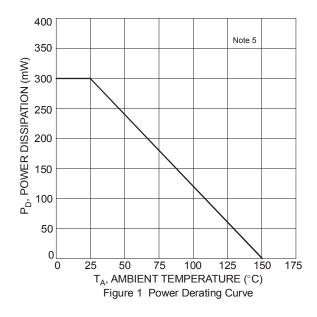
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

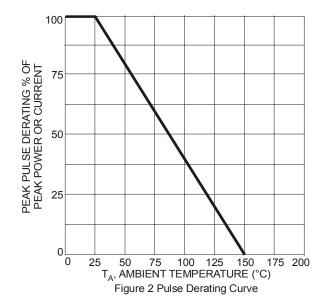
6. Short duration pulse test used to minimize self-heating effect.

7. Clamping voltage value is based on an  $8x20\mu s$  peak pulse current ( $I_{pp}$ ) waveform.

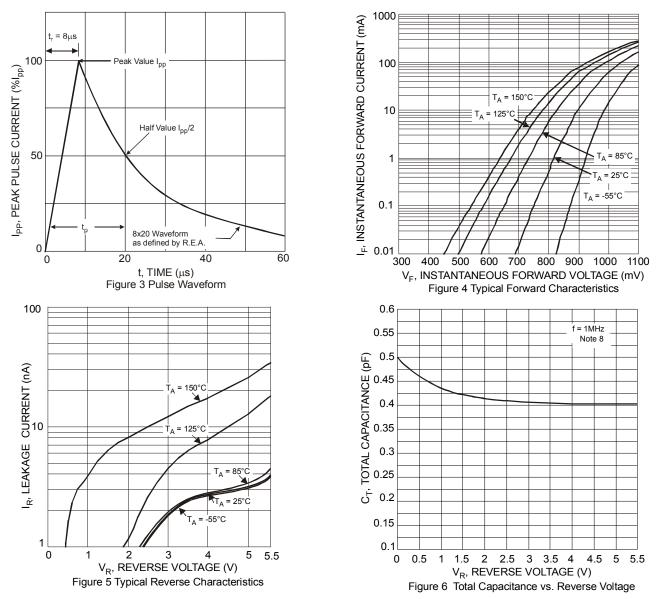
8. Measured from any I/O to GND.

9. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destools/appnote\_dnote.html.



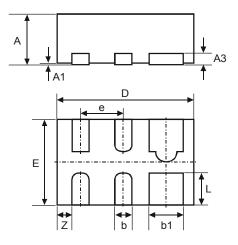






# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

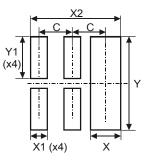


U-DFN1610-6						
Dim	Min	Max	Тур			
Α	0.545	0.605	0.575			
A1	0	0.05	0.03			
A3	_	_	0.13			
b	0.15	0.25	0.20			
b1	0.35	0.45	0.40			
D	1.550	1.675	1.600			
e			0.5			
Е	0.950	1.075	1.000			
L	0.325	0.425	0.375			
Ζ			0.150			
All	Dimens	sions in	mm			



# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.500
Х	0.450
X1	0.250
X2	1.350
Y	1.400
Y1	0.625

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